

FIG. 3A

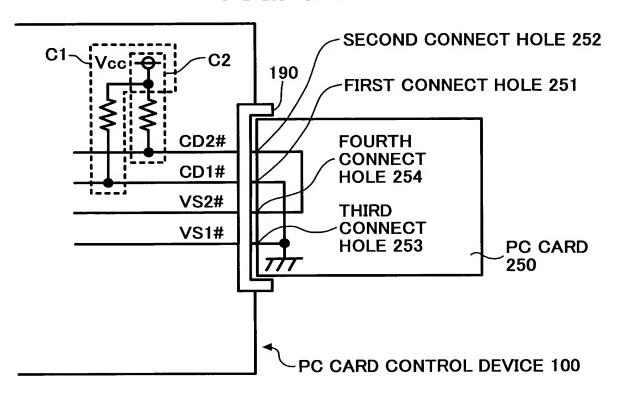


FIG. 3B

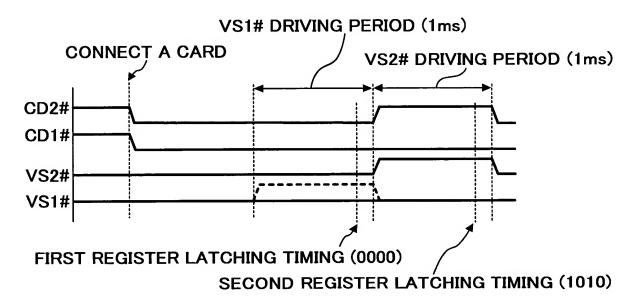


FIG. 4A

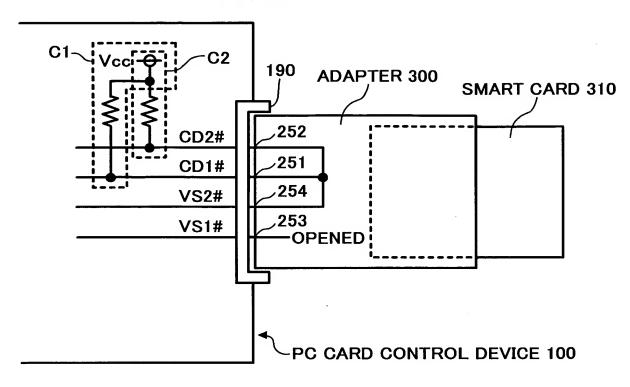


FIG. 4B

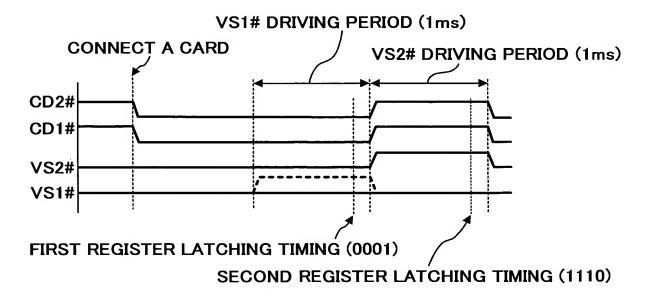


FIG. 5A

i. 5	FIG. 5B	FIG. 5D
FIG	IG. 5A	IG. 5C

				······································		
VS2# FLOWING FOURTH CONNECT HOLE	GROUNDED	GROUNDED	OPENED	OPENED	GROUNDED	OPENED
CD1# FLOWING FIRST CONNECT HOLE	GROUNDED	GROUNDED	GROUNDED	GROUNDED	CONNECTED WITH THIRD CONNECT HOLE	CONNECTED WITH THIRD CONNECT HOLE
CD2# FLOWING SECOND CONNECT HOLE	GROUNDED	GROUNDED	GROUNDED	GROUNDED	GROUNDED	GROUNDED
NO.	-	2	က	4	2	9

FIG. 5B

SECOND	REGISTER STATE	0000	0000	0010	0010	0000	0010
FIRST	REGISTER STATE	0000	0001	0000	0001	0101	0101
F CARD	DRIVING VOLTAGE V	5V, 3.3V, X. XV	X. XV	5V, 3.3V	Λ9	RESERVED	3.3V
TYPE OF CARD	INTERFACE	16BIT	16BIT	16BIT	16BIT	RESE	CARDBUS
VS1# FI OWING	THIRD CONNECT HOLE	GROUNDED	OPENED	GROUNDED	OPENED	CONNECTED WITH FIRST CONNECT HOLE	CONNECTED WITH FIRST CONNECT HOLE

7	CONNECTED WITH FOURTH CONNECT HOLE	GROUNDED	CONNECTED WITH SECOND CONNECT HOLE
8	CONNECTED WITH FOURTH CONNECT HOLE	GROUNDED	CONNECTED WITH SECOND CONNECT HOLE
6	GROUNDED	CONNECTED WITH FOURTH CONNECT HOLE	CONNECTED WITH FIRST CONNECT HOLE
10	GROUNDED	CONNECTED WITH FOURTH CONNECT HOLE	CONNECTED WITH FIRST CONNECT HOLE
11	CONNECTED WITH THIRD CONNECT HOLE	GROUNDED	GROUNDED
12	CONNECTED WITH THIRD CONNECT HOLE	GROUNDED	OPENED

FIG. 5D

GROUNDED	CARDBUS	3.3V, X. XV	0000	1010
OPENED	CARDBUS	X. XV	0001	1010
GROUNDED	RESE	RESERVED	0000	0110
OPENED	CARDBUS	X. XV, Y. YV	0001	0110
CONNECTED WITH SECOND CONNECT HOLE	CARDBUS	3.3V, X. XV Y. YV	1001	0000
CONNECTED WITH SECOND CONNECT HOLE	CARDBUS	Y. YV	1001	0010

FIG. 6A

eA

FIG.

FIG. 6

FIG. 6B

FOURTH CONNECT HOLE FIRST AND SECOND FIRST AND SECOND CONNECTED WITH **CONNECT HOLES** CONNECTED WITH SECOND CONNECT CONNECTED WITH CONNECTED WITH **CONNECT HOLES VS2# FLOWING** FIRST CONNECT GROUNDED **OPENED** HOLE HOLE FOURTH CONNECT CONNECTED WITH THIRD CONNECT. FOURTH CONNECT CONNECTED WITH CONNECTED WITH CONNECTED WITH CONNECTED WITH CONNECTED WITH FOURTH CONNECT THIRD CONNECT THIRD CONNECT FIRST CONNECT CD1# FLOWING HOLE HOLE HOLE HOLE HOLE HOLE HOLE SECOND CONNECT HOLE FOURTH CONNECT CONNECTED WITH FOURTH CONNECT CONNECTED WITH CONNECTED WITH CONNECTED WITH CONNECTED WITH FOURTH CONNECT CONNECTED WITH THIRD CONNECT CD2# FLOWING THIRD CONNECT THIRD CONNECT HOLE HOLE HOLE HOLE HOLE HOLE S. 2 က 4 S 9

FIG. 6B

VS1# FLOWING	TYPE OF CARD	FIRST	SECOND
THIRD CONNECT HOLE	INTERFACE DRIVING VOLTAGE V	REGISTER STATE	REGISTER STATE
CONNECTED WITH FIRST CONNECT HOLE	NEW TYPE NO. 1	0101	1010
CONNECTED WITH SECOND CONNECT HOLE	NEW TYPE NO. 2	1001	0110
CONNECTED WITH FIRST AND SECOND CONNECT HOLES	NEW TYPE NO. 3	1101	0000
CONNECTED WITH FIRST AND SECOND CONNECT HOLES	NEW TYPE NO. 4	1101	0010
GROUNDED	NEW TYPE NO. 5	0000	1110
OPENED	NEW TYPE NO. 6	0001	1110

FIG. 7A

FIG. 7

FIG. 7A

FIG. 7B

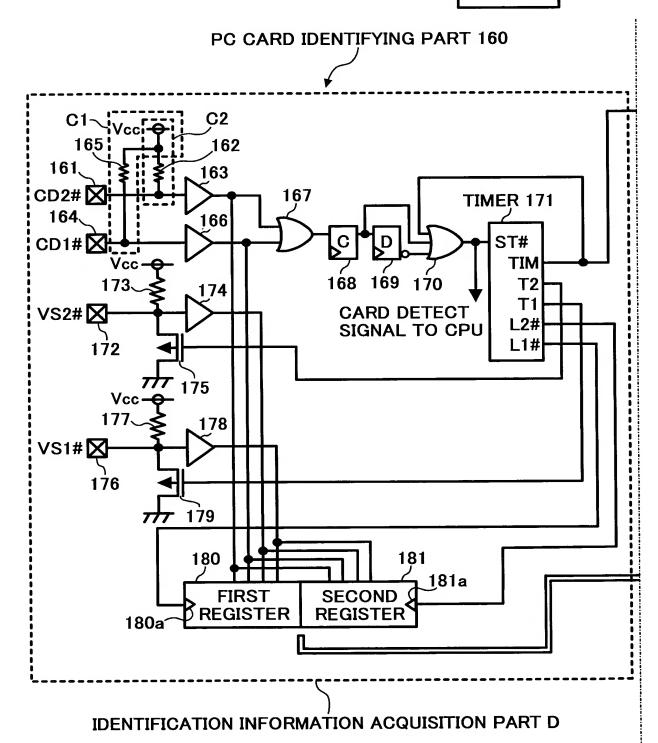
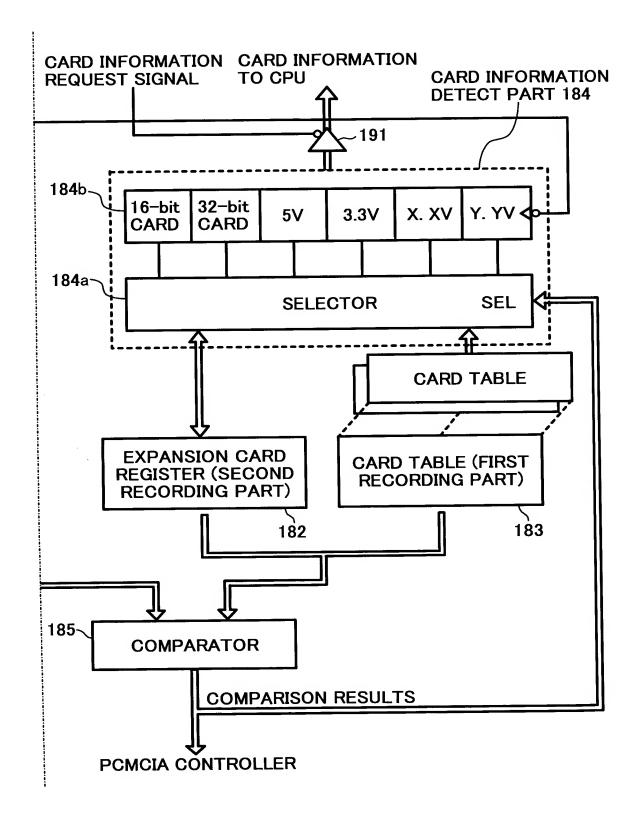
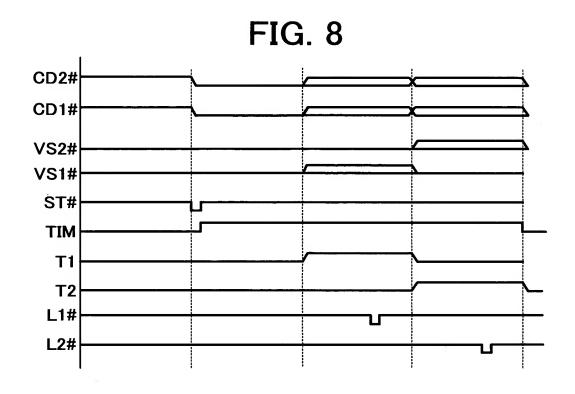


FIG. 7B





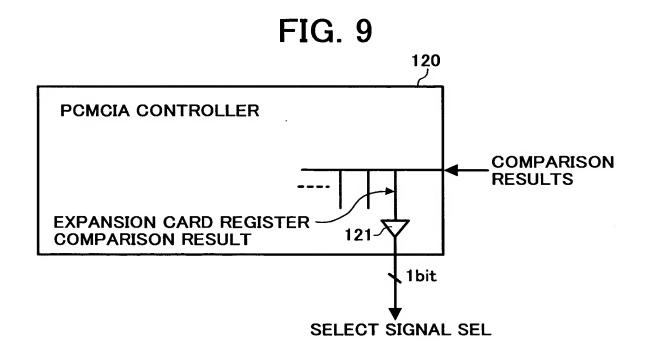
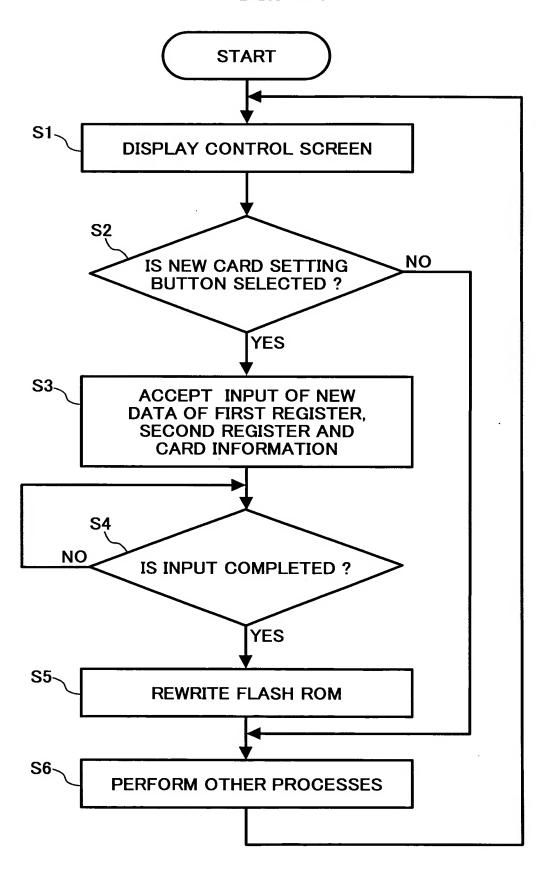


FIG. 10



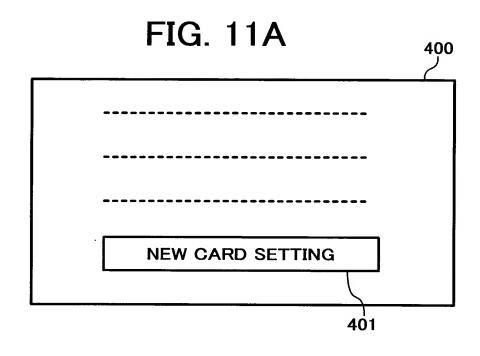


FIG. 11B

FIRST REGISTER

SECOND REGISTER

CARD TYPE SPECIFYING DATA

COMPLETION

410

411

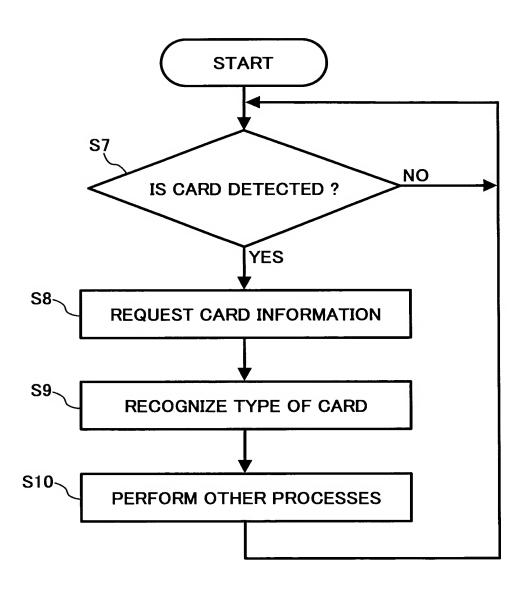
412

CARD TYPE SPECIFYING DATA

COMPLETION

414

FIG. 12



COMPARISON RESULTS 182 CARD DETECT SIGNAL CARD INFORMATION MEMORY **CARD TABLE** CARD INFORMATION EXPANSION CARD REGISTER -CARD INFORMATION REQUEST SIGNAL PC CARD IDENTIFYING PART 160a CONTROLLER (CPU) 981 162 63 991 φ Ccc **Φ**33/ 173 CD1# 🔯 **№** #1SA CD2# 📉 164 161

CARD TABLE

181a

SECOND REGISTER

FIRST REGISTER

180

181

179

180a~

183

FIG. 13

FIG. 14A

FIG. 14

FIG. 14A

FIG. 14B

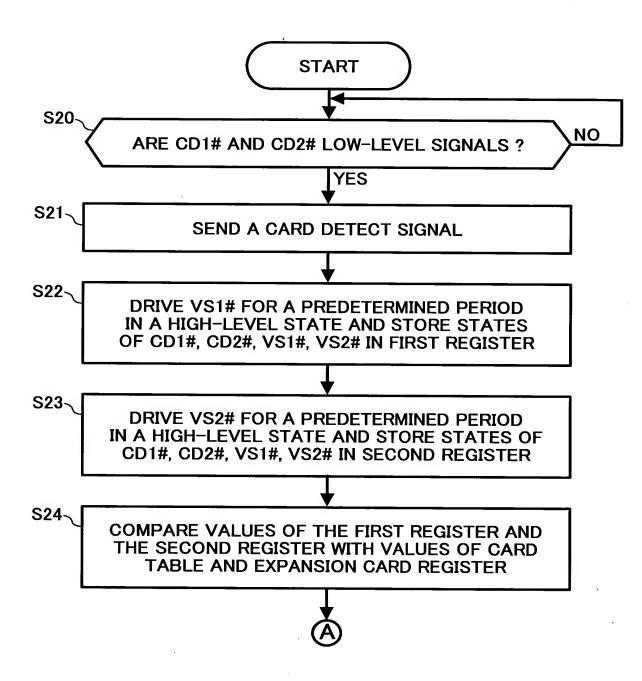
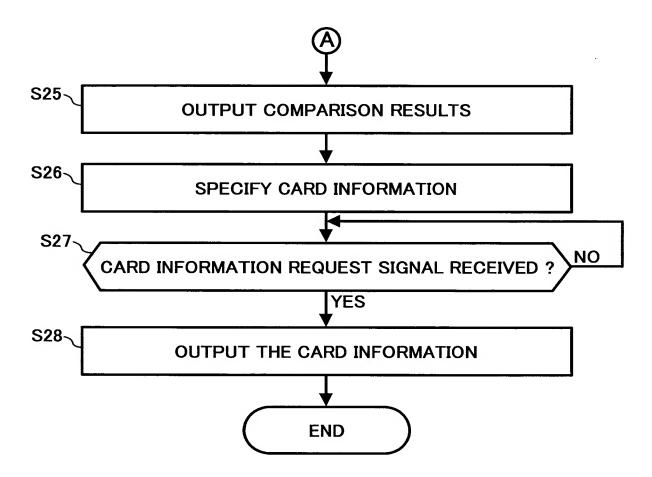


FIG. 14B



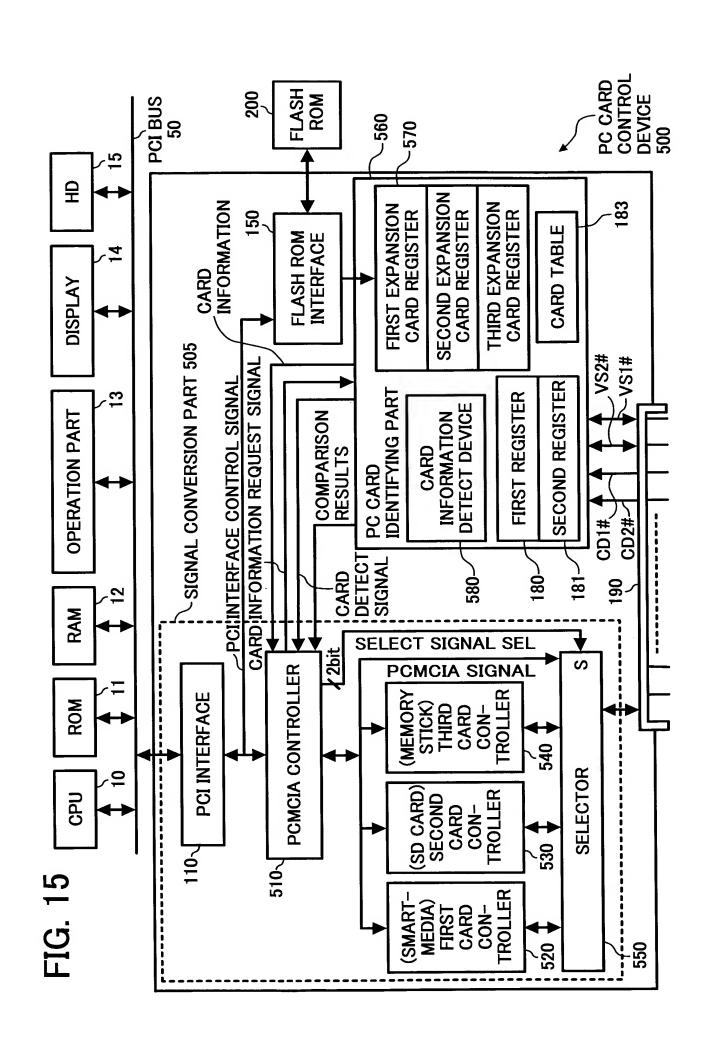


FIG. 16A

FIG. 16

FIG. 16A

FIG. 16B

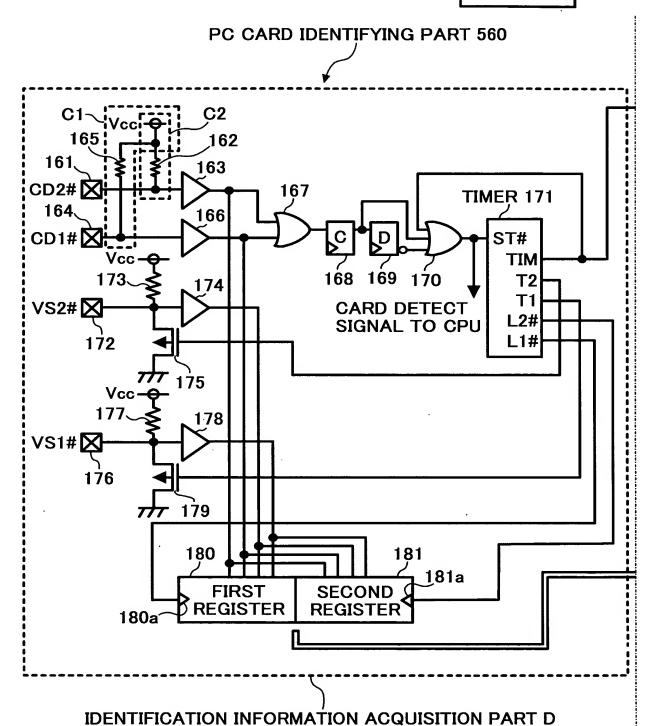


FIG. 16B

